PATENT Page 18 Page 18

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

re Application of

Customer Number: 20277

Hidemoto TOMITA, et al.

Confirmation Number: 2722

Serial No.: 10/670,396

Group Art Unit: 2811

Filed: September 26, 2003

Examiner: To be Assigned

For:

SEMICONDUCTOR MEMORY DEVICE

## INFORMATION DISCLOSURE STATEMENT

Mail Stop IDS Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

In a ccordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached form PTO-1449. It is respectfully requested that the documents be expressly considered during the prosecution of this application, and that the documents be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No certification or fee is required.

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Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

MCDERMOTT, WILL & EMERY

ephen A. Becker Registration No. 26,527

600 13<sup>th</sup> Street, N.W. Washington, DC 20005-3096 (202) 756-8000 SAB:mcw

Facsimile: (202) 756-8087

Date: January 21, 2004

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